ABSTRACT OF THE DISCLOSURE

An ATM switching equipment is provided comprising a switching network (ASN) and at least one server switching unit (SSC). The server switching unit (SSC) contains an AAL2 switcher (ASW) that is configured for simultaneous processing of a maximum plurality (m) of incoming connections. An AAL2 routing list (RL1...RLm) is provided for each of these connections, and a microprocessor (MPR) is configured to limit the allowable value range for VPI/VCI values in the header of the ATM cells according to the plurality of AAL2 routing lists, so that only the corresponding VPI/VCI coding bits are taken into consideration at an interface to the switching network.